

REMARKS

Careful consideration has been given by applicants to the Examiner's comments and formal grounds of rejection of the application, as set forth in the outstanding Office Action, and on the basis of the enclosed amendments, the early and favorable consideration of the application and allowance thereof, respectfully solicited.

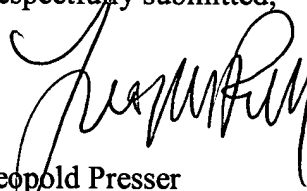
Concerning the claims, Claims 1 and 26 have been amended to indicate the structure of the "mask", which represents the metallic wiring line layer, forming a wire loop, as shown in Figures 1 and 2. Thus, when placed in a magnetic field, a voltage is induced between the ends 12 and 14 of the wire loop 16. This construction is clearly shown in Figures 1 and 2 of the drawings. Moreover, a detailed description thereof is set forth in Figures 9 and 10 and also described on page 26 of the specification. Furthermore, in order to clarify the foregoing, although discussed in detail in the application, applicants have included proposed drawing revisions introducing enlarged Figure 3b, showing the chip of Figures 1 and 2, as positioned on the wafer shown in Figure 3a, and a further enlarged detail in new Figure 3c, showing the wafer of Figure 3a, the chips on the wafer and the mask with wire loop shown on each chip.

This particular construction has now been set forth in the claims, and no new matter has been entered herein. Furthermore, the electrical contacts are shown in Figures 1 and 2 as Items 12 and 14. These two ends of the wire loop 16 provide for electrical connections to the chip through the decal mask, as referred to in Figures 9 and 10. Furthermore, this is supported by the specification on page 15, lines 5-8. These electrical contacts are more clearly identified (in addition to Figures 1 and 2) in the proposed Figure 3b and new Figure 3c.

Furthermore, with regard to the Examiner's query as to what the term "circuit represents" means in Claims 6 and 31, this represents the open circuit metallic wire loop, as shown in Figures 1 and 2, enclosing an area A and with electrical contacts 12 and 14 of the loop at the ends thereof, as shown in Figures 3a-3c.

Accordingly, inasmuch as the applicants have fully complied with the requirements of the Office Action, and this particular structure has now been identified in the claims and in the drawings without the introduction of any new matter, the early and favorable examination of the application on the merits and allowance thereof by the Examiner is earnestly solicited. However, in the event that the Examiner has any queries concerning the instantly submitted Amendment, applicants' attorney respectfully requests that he be accorded the courtesy of possibly a telephone conference to discuss any matters in need of attention.

Respectfully submitted,



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Enclosures: Annotated Sheets
(Amended Figure 6; New Figures 3b and 3c)